REMARKS

Claims 1-29 are currently pending in the subject application, and are presently under consideration. Claims 18-20 and 25-29 are allowed. Claims 1, 3, 10, 12, 14, 16, 17 and 21 are rejected. Claims 2, 4-9, 11, 13, 15 and 22-24 have been indicated as allowable. Favorable reconsideration of the application is requested in view of the amendments and comments herein.

I. Rejection of Claims 1, 3, 10, 14, 16 and 17 Under 35 U.S.C. §102

Claims 1, 3, 10, and 14 stand rejected under 35 U.S.C. §102 as being anticipated by U.S. Patent No. 5,963,091 to Chen, et al. ("Chen"), Figure 2. Withdrawal of this rejection is respectfully requested for at least the following reasons.

Claim 1 has been amended to recite a cross cancellation component that generates a digital reference signal that is an undistorted version of a desired output signal, the digital reference signal being converted to an analog signal, amplified and employed to determine an error signal that is added to the amplifier output signal to substantially reduced unwanted signals, distortion and out-of-band (OOB) emissions associated with the amplified output signal.

Chen et al. discloses adding an unmodified and amplified analog input signal from a first analog loop with an analog error cancellation signal from a second loop. The analog error cancellation signal is derived from a combination of the analog input signal, an analog cancellation adjustment signal α , and an analog error cancellation adjustment signal β . The analog cancellation adjustment signal α , and the analog error cancellation adjustment signal β are computed employing the analog input signal, the analog output signal and an error signal.

Chen et al. does not disclose a digital reference signal that is an undistorted version of a desired output signal, the digital reference signal being converted to an analog signal, amplified and employed to determine an error signal that is added to the amplifier output signal to substantially reduced unwanted signals, distortion and out-of-band (OOB) emissions associated with the amplified output signal digital signal is directly converted by a digital to analog converter and added to an amplifier output signal, as recited in amended claim 1.

Therefore, Chen et al. does not teach each and every element of claim 1, and does not anticipate claim 1. Claims 3, 10, 12, 14 and 16-17 depend directly or indirectly from claim 1, and therefore are not anticipated by Chen et al.

For the reasons described above, claims 1, 3, 10, and 14-should be patentable over the cited art. Accordingly, withdrawal of this rejection is respectfully requested.

II. Rejection of Claim 21 Under 35 U.S.C. §102

Claim 21 stands rejected under 35 U.S.C. §102 as being anticipated by U.S. Patent No. 6,934,341 to Sahlman, ("Sahlman"), Figure 4.

Claim 21 has been amended to include the elements recited in allowable claim 23, and claim 23 has been cancelled. Accordingly, withdrawal of this rejection is respectfully requested.

III. Rejection of Claim 12 Under 35 U.S.C. §103(a)

Claim 12 stands rejected under 35 U.S.C. §103(a) as being unpatentable over Chen in view of U.S. Patent No. 5,757,229 to Mitzlaff ("Mitzlaff"). Withdrawal of this rejection is respectfully requested for at least the following reasons.

Claim 12 depends from claim 1. Mitzlaff does not make up for the aforementioned deficiencies of Chen with respect to claim 1, which claim 12 depends. Therefore, Chen in view of Mitzlaff does not make obvious claim 12.

For the reasons described above, claim 12 should be patentable over the cited art. Accordingly, withdrawal of this rejection is respectfully requested.

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CONCLUSION

In view of the foregoing remarks, Applicant respectfully submits that the present application is in condition for allowance. Applicant respectfully requests reconsideration of this application and that the application be passed to issue.

Please charge any deficiency or credit any overpayment in the fees for this amendment to our Deposit Account No. 20-0090.

Respectfully submitted,

Date 4-17-06

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